10/719,663

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SEP 26 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:	Sadeg M. Faris) Group Art Unit
SERIAL NO.:	10/719,663	
FILING DATE:	November 20, 2003) Examiner) TBD
FOR:	Method of Fabricating Multi Layer Devices on Buried Oxide Layer Substrates)

Mail Stop Missing Parts Commissioner of Patents PO Box 1450 Alexandria, VA 22313-1450

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SEP 27 2005

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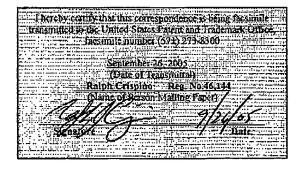
No. 4641

RESPONSE TO RESTRICTION REQUIREMENT

Sirs:

In response to the restriction requirement mailed July 27, 2005 in the above application, Applicants respectfully submit the herein election. Applicant hereby petitions for and requests a one -month extension of time to extend the period for response up to and including September 27, 2005. The Commissioner is hereby authorized to charge the \$60.00 extension fee for a small entity to Deposit Account No. 501648. Applicant is a Small Entity.

Applicants hereby elects group Species I. Accordingly, claims 6-13 are withdrawn. A complete listing of the pending claims is presented herein.



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